

FIG. 1

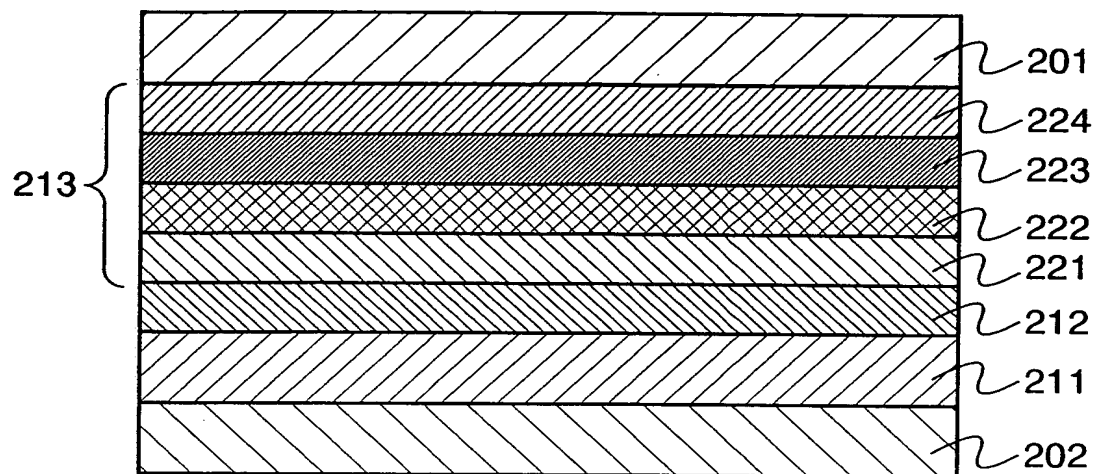


FIG. 2

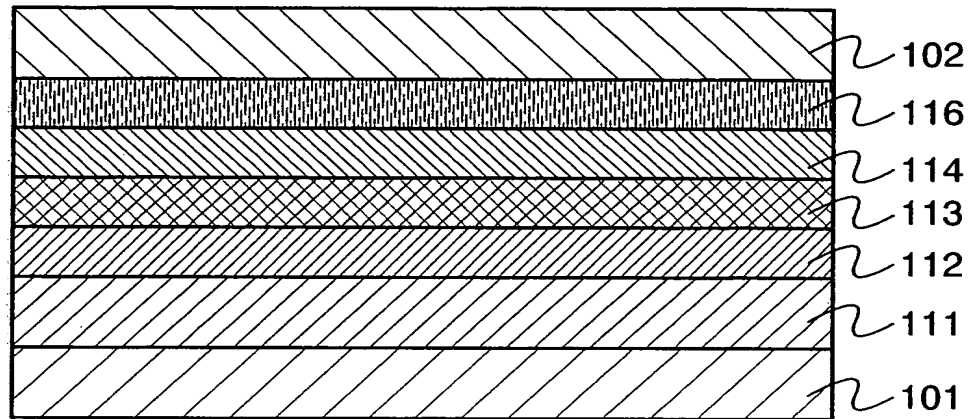


FIG. 3

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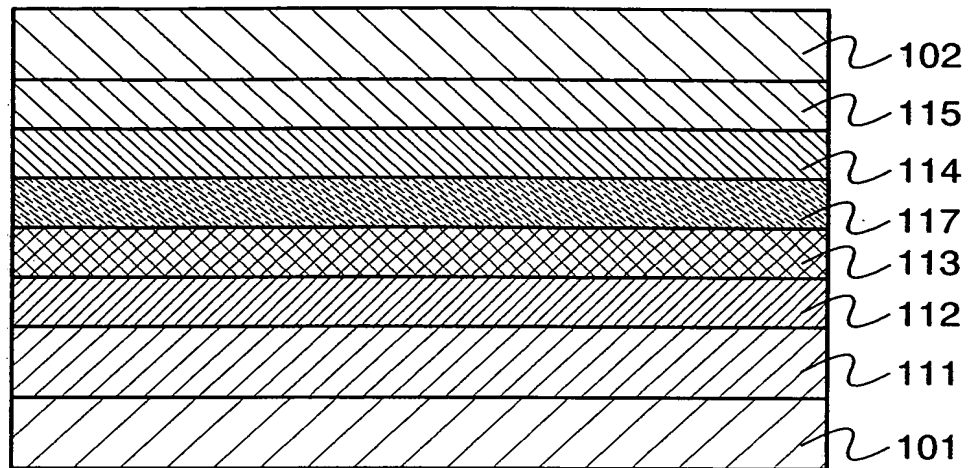


FIG. 4

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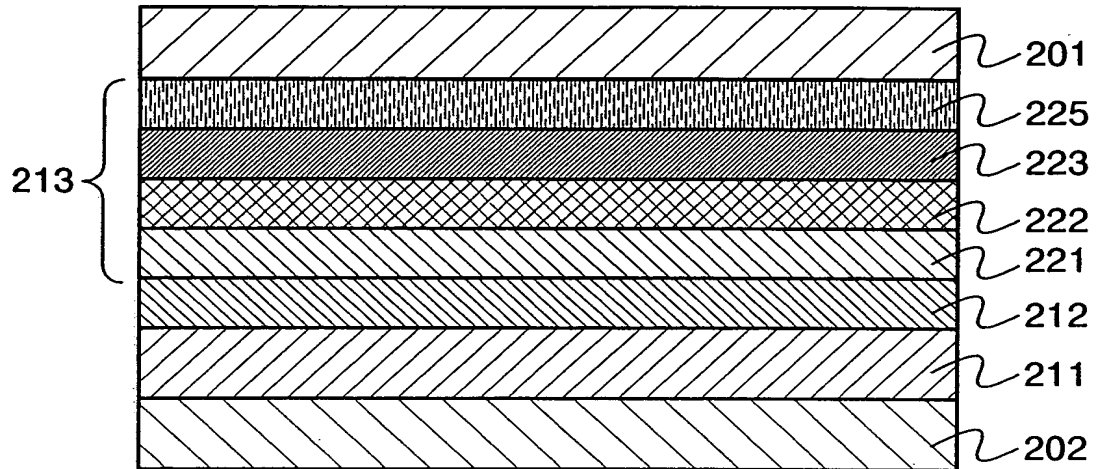


FIG. 5

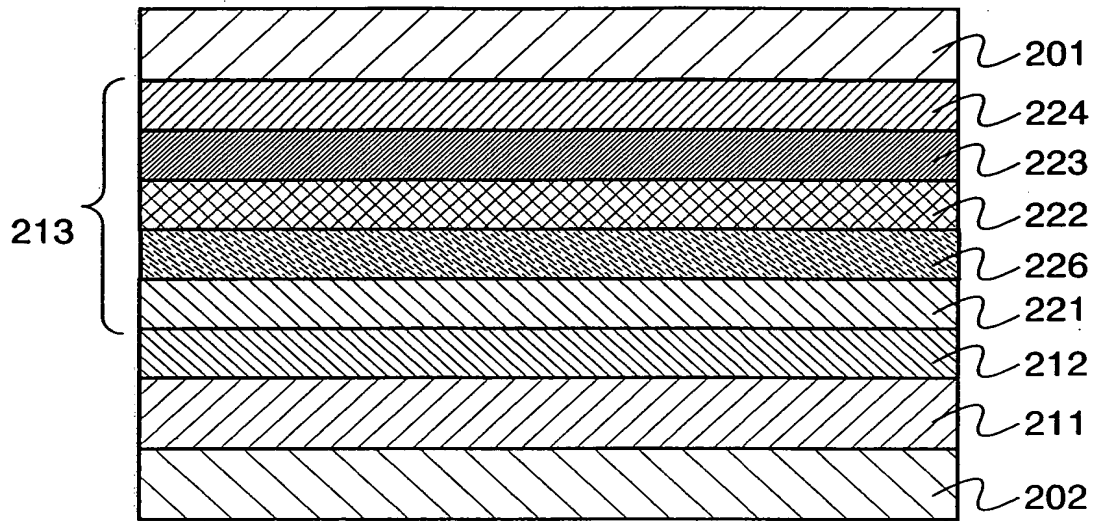


FIG. 6

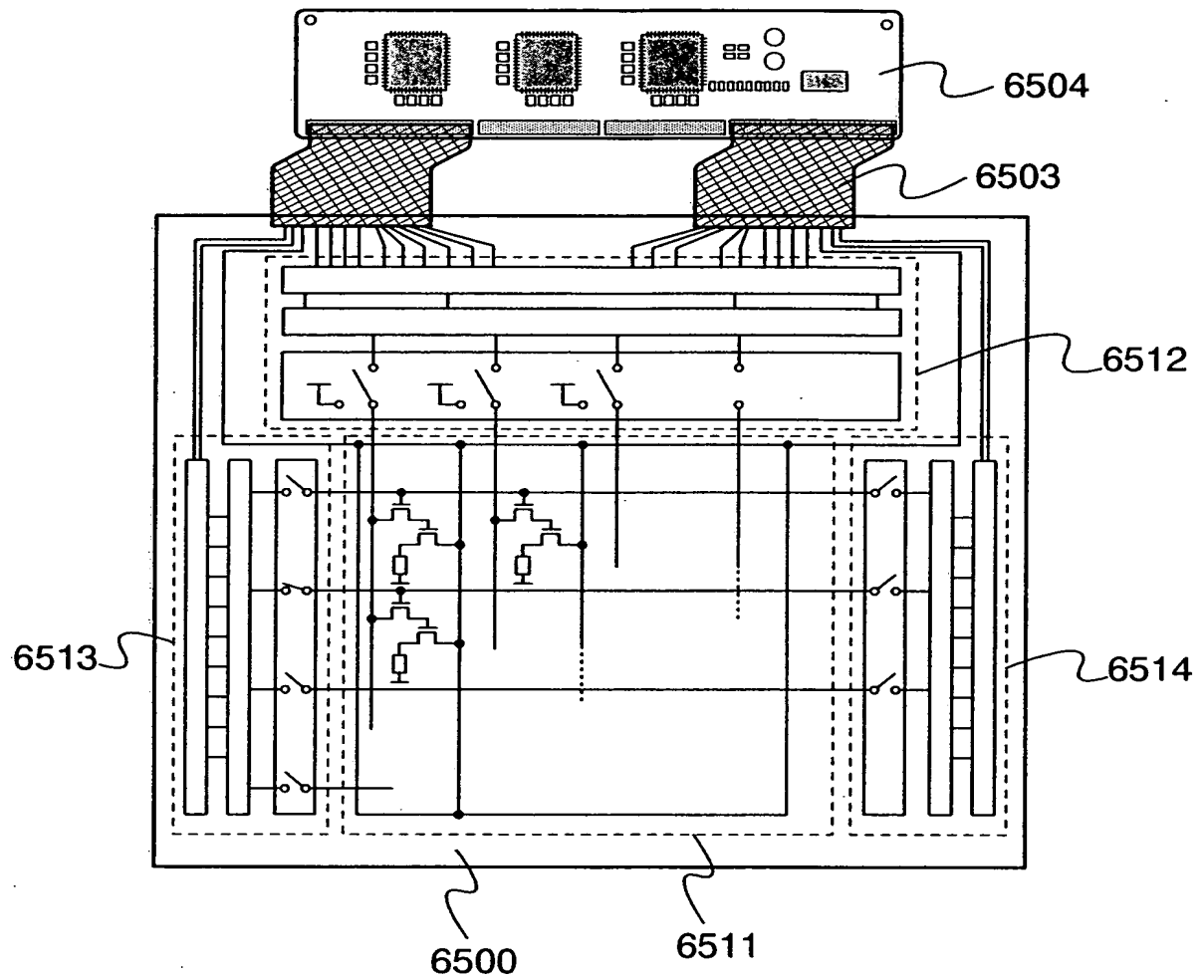


FIG. 7

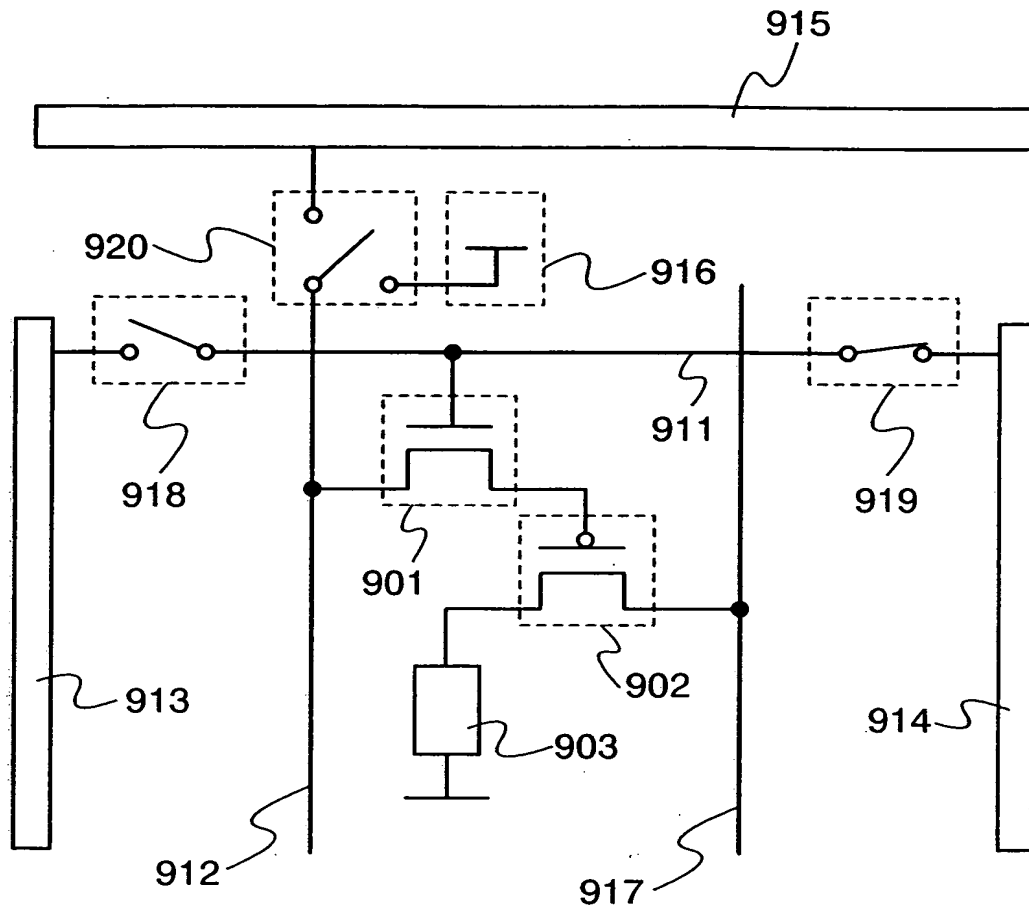


FIG. 8

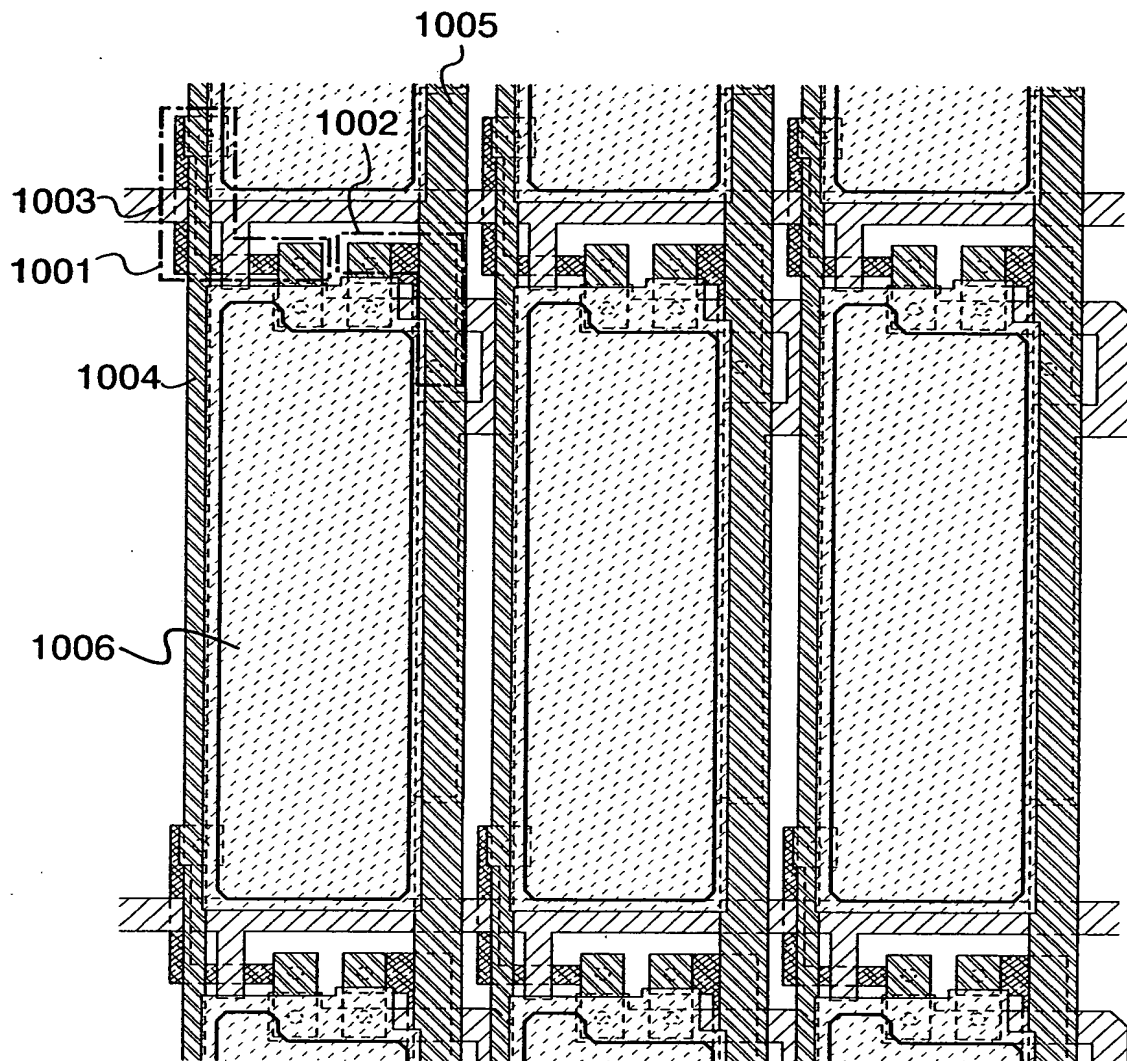


FIG. 9

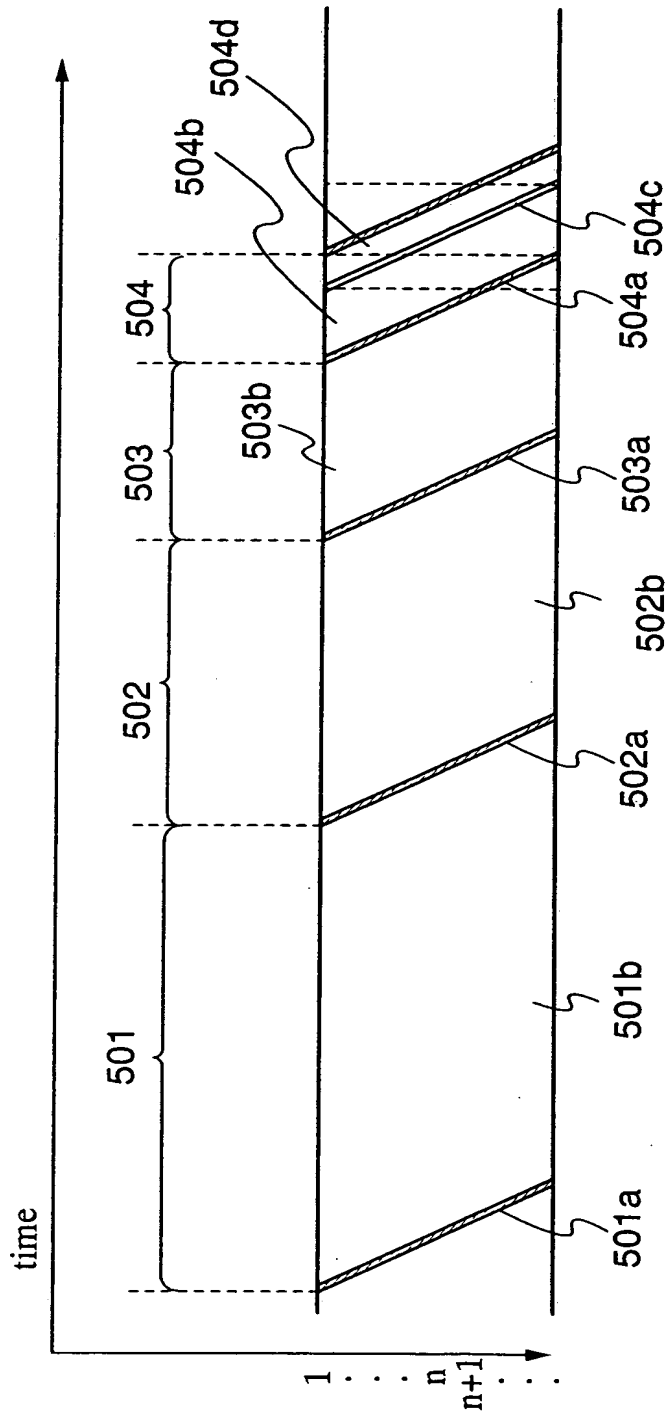
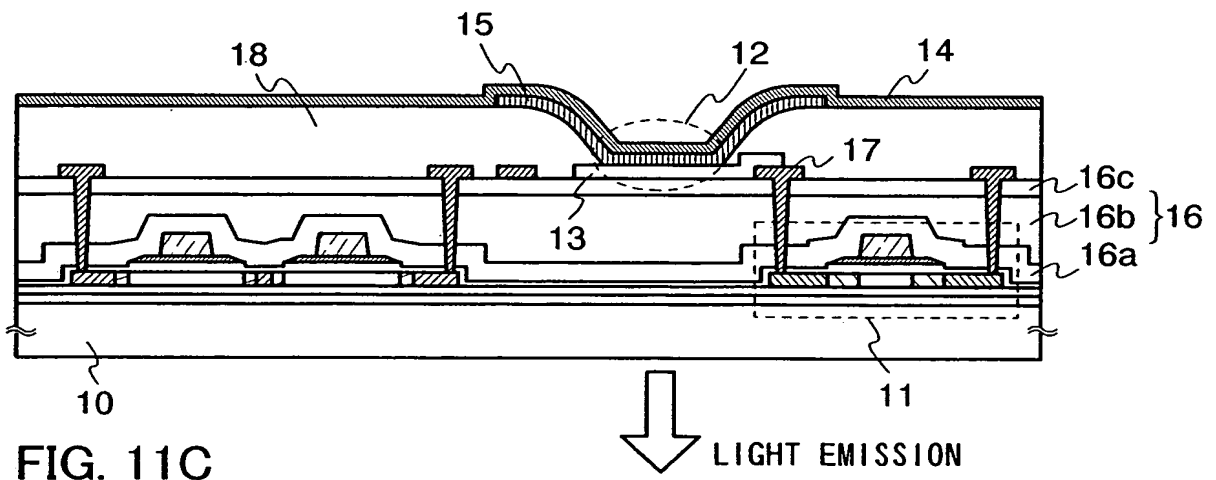
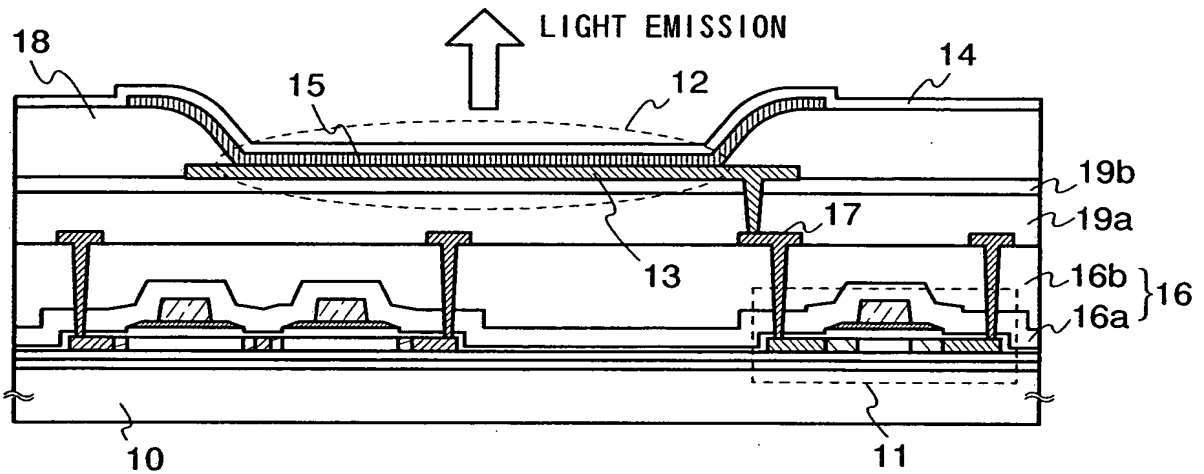
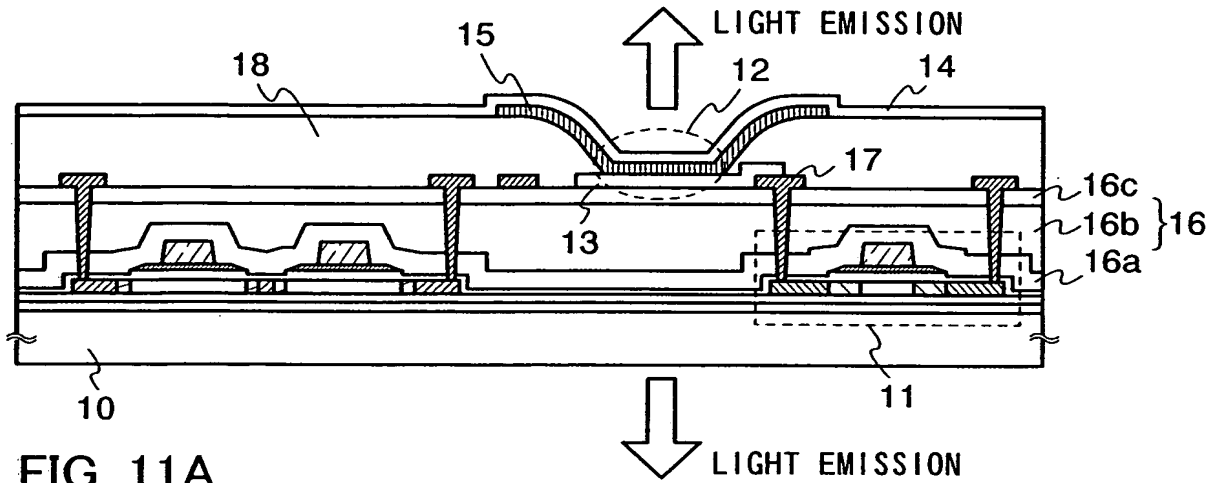


FIG. 10



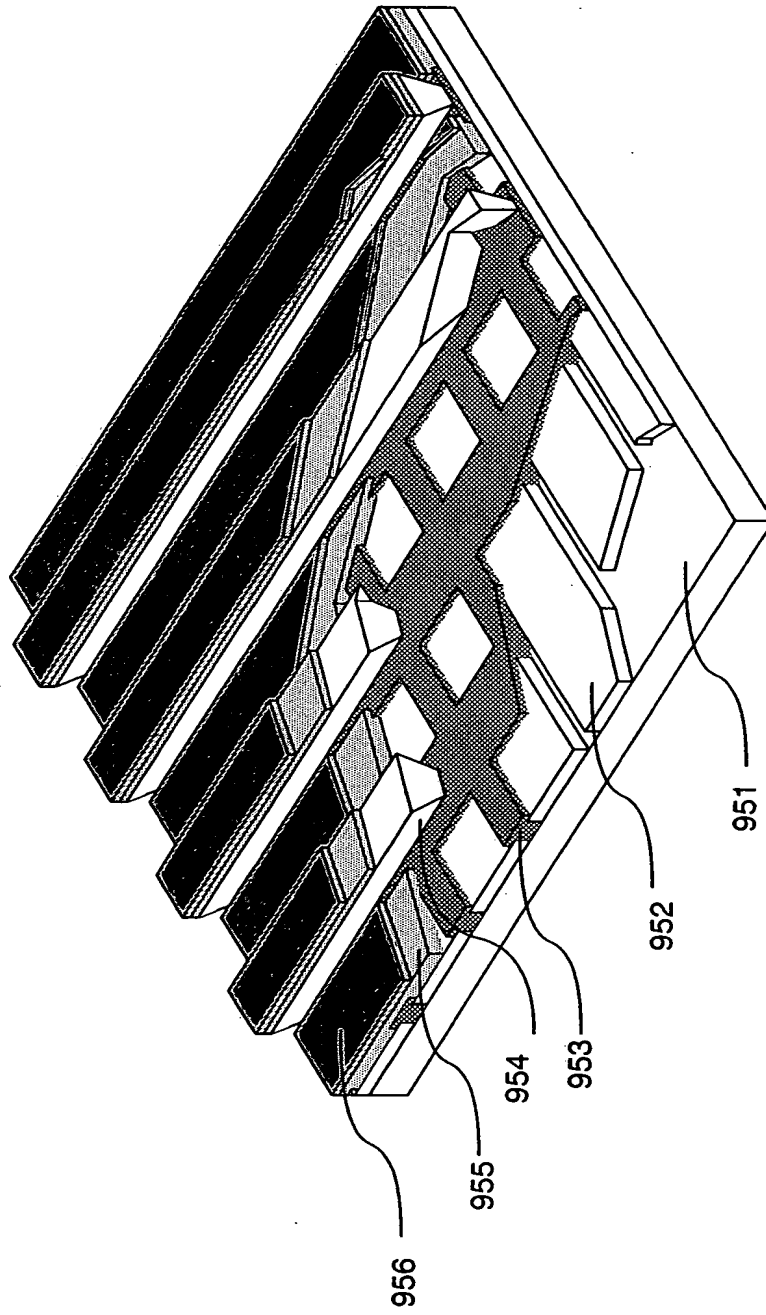


FIG. 12

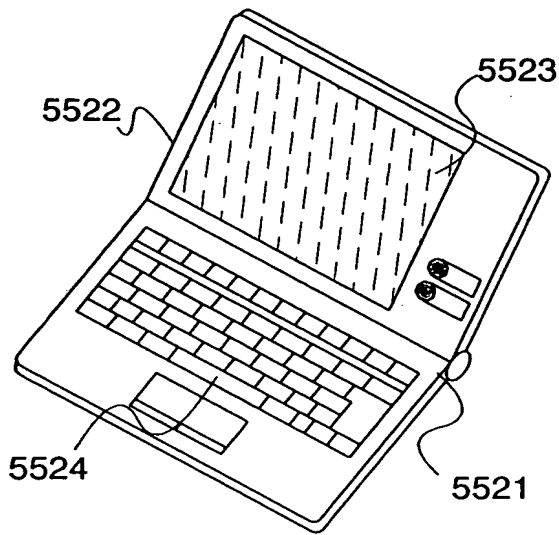


FIG. 13A

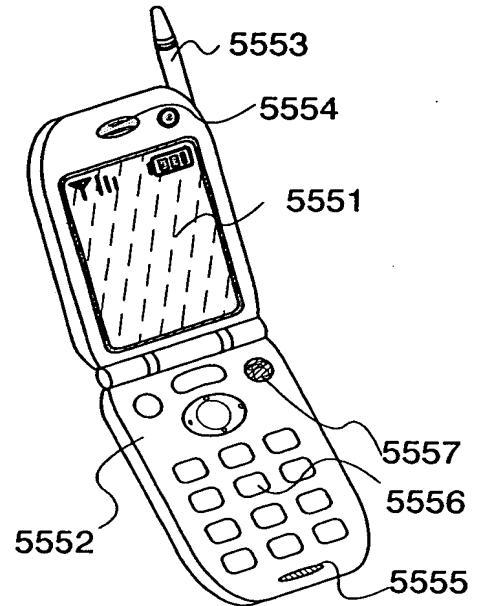


FIG. 13B

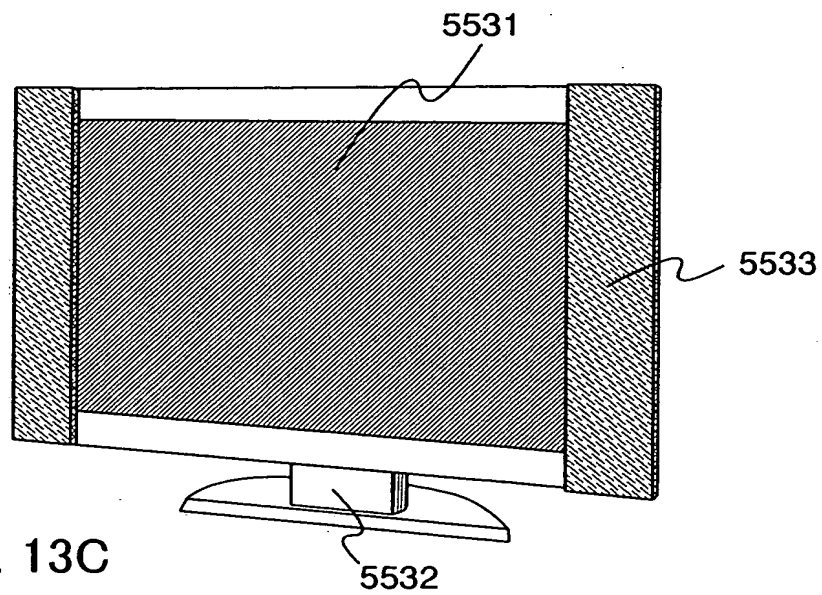


FIG. 13C

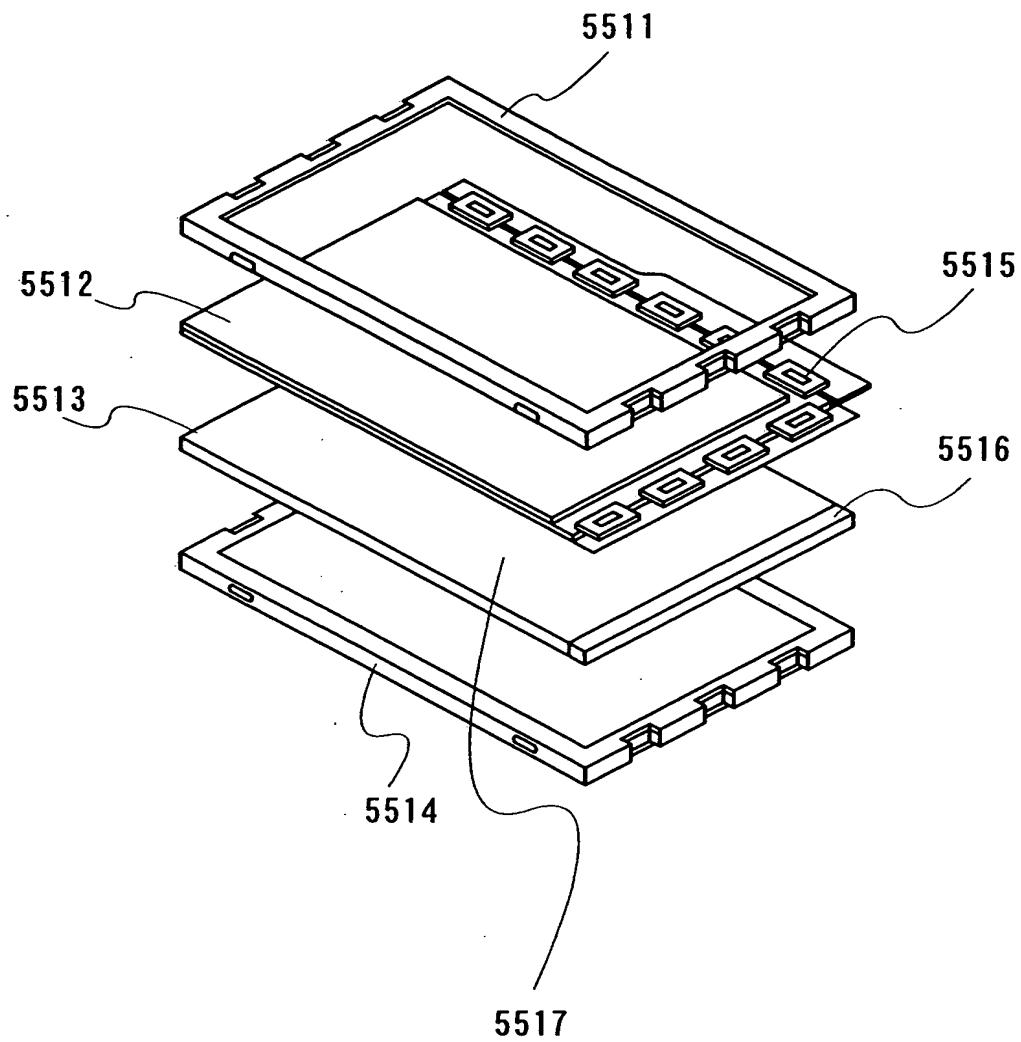


FIG. 14

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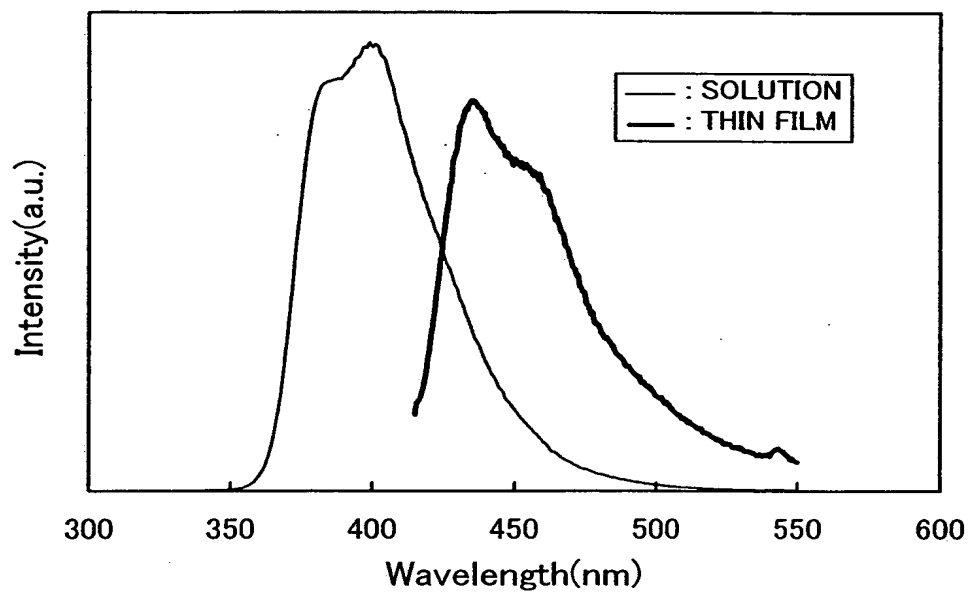


FIG. 15

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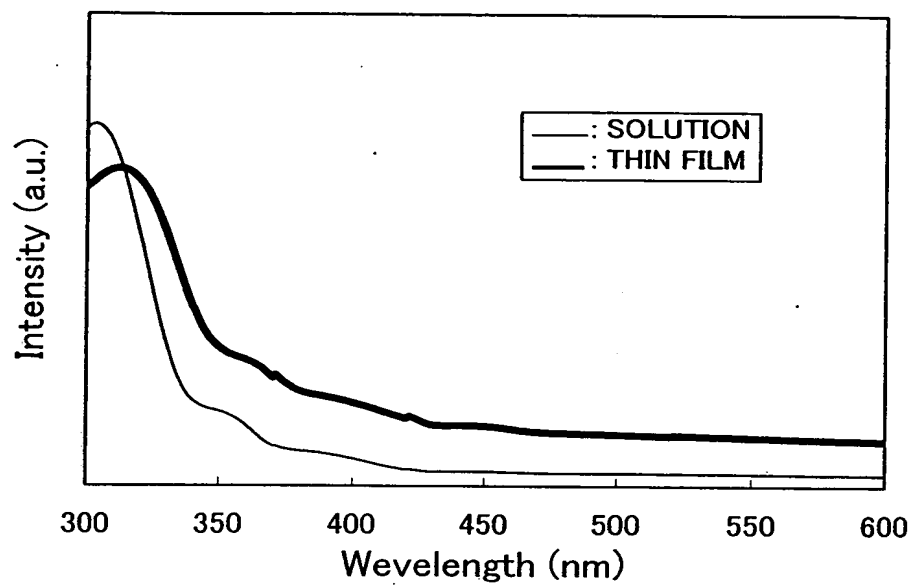


FIG. 16

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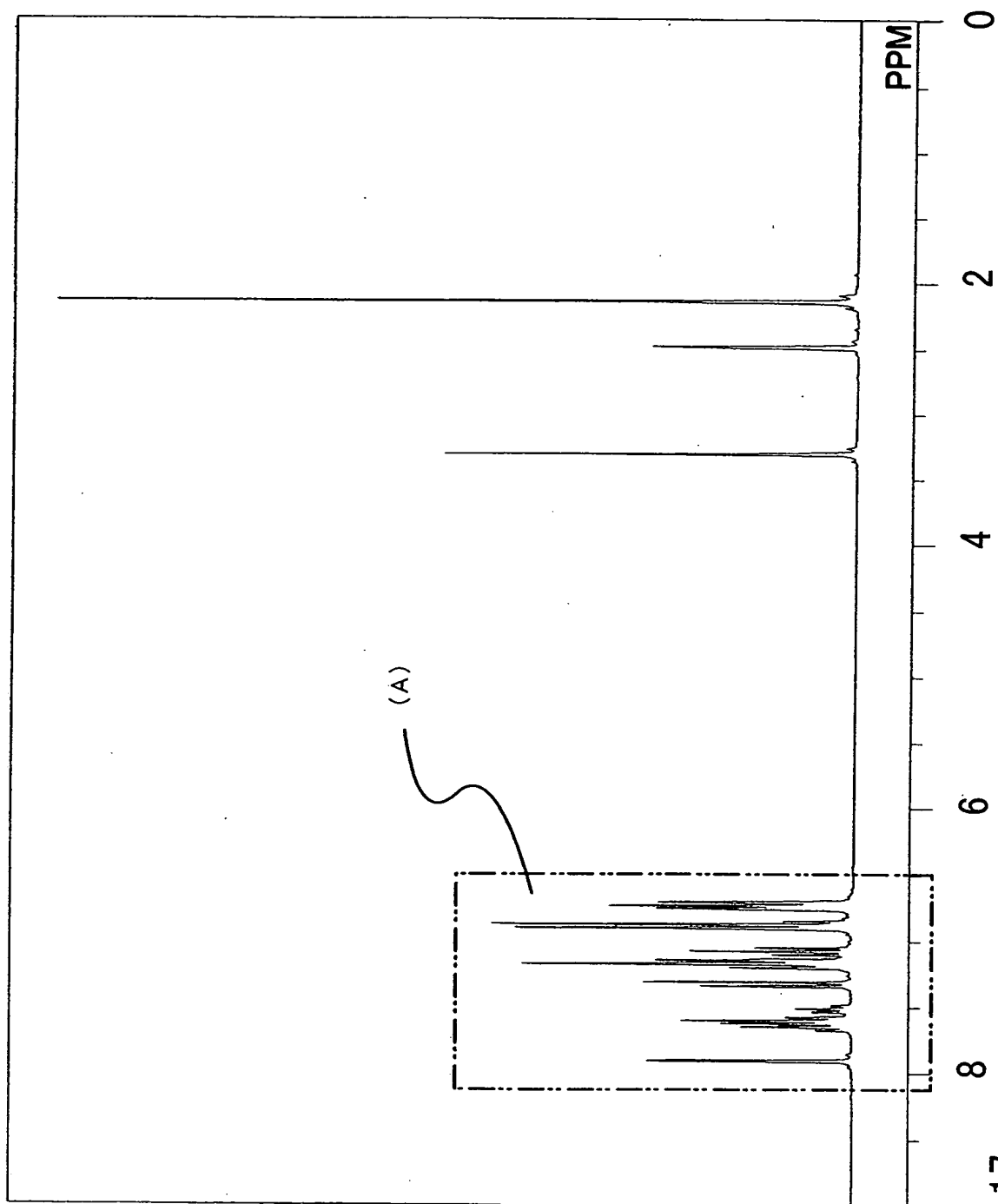


FIG. 17

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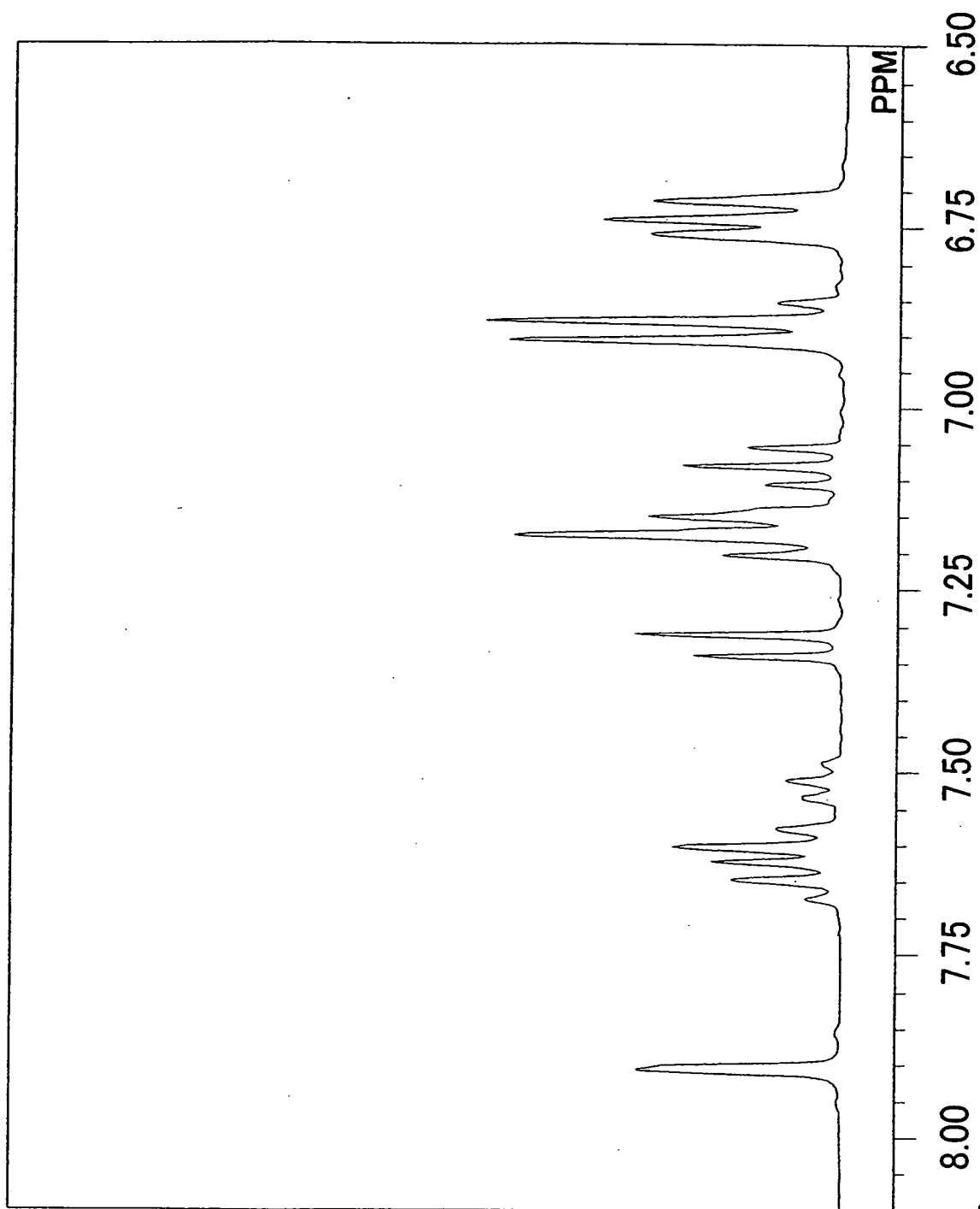


FIG. 18

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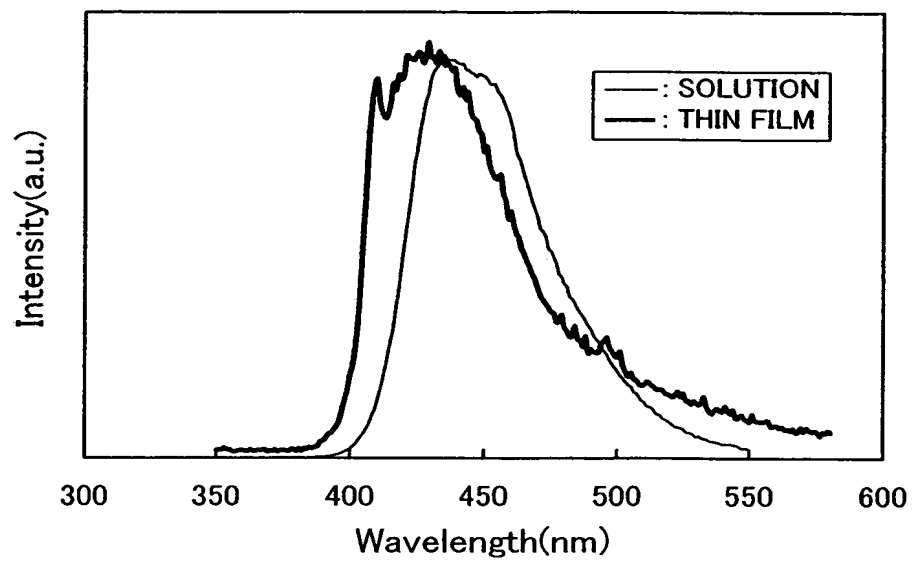


FIG. 19

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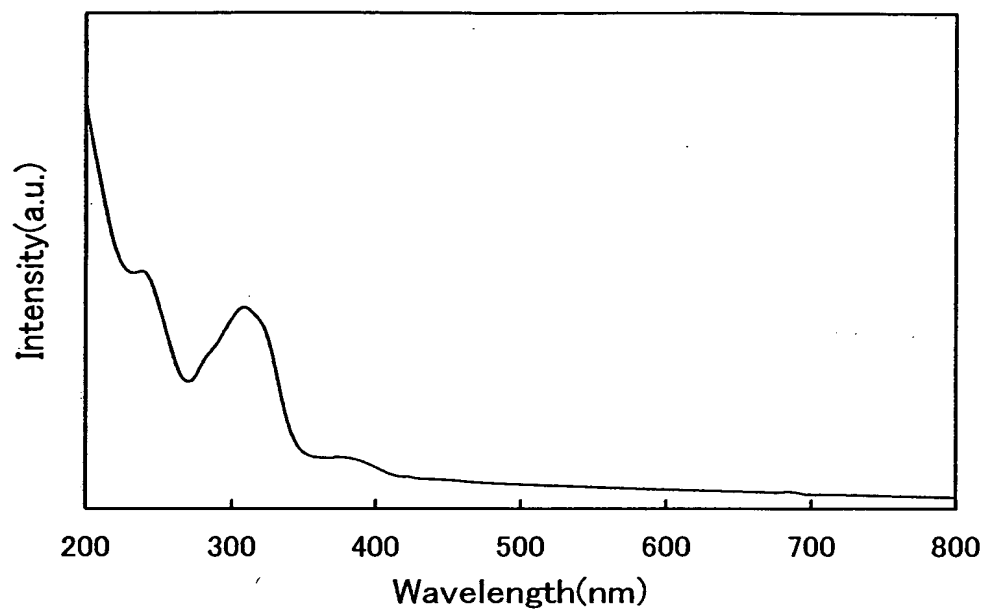


FIG. 20

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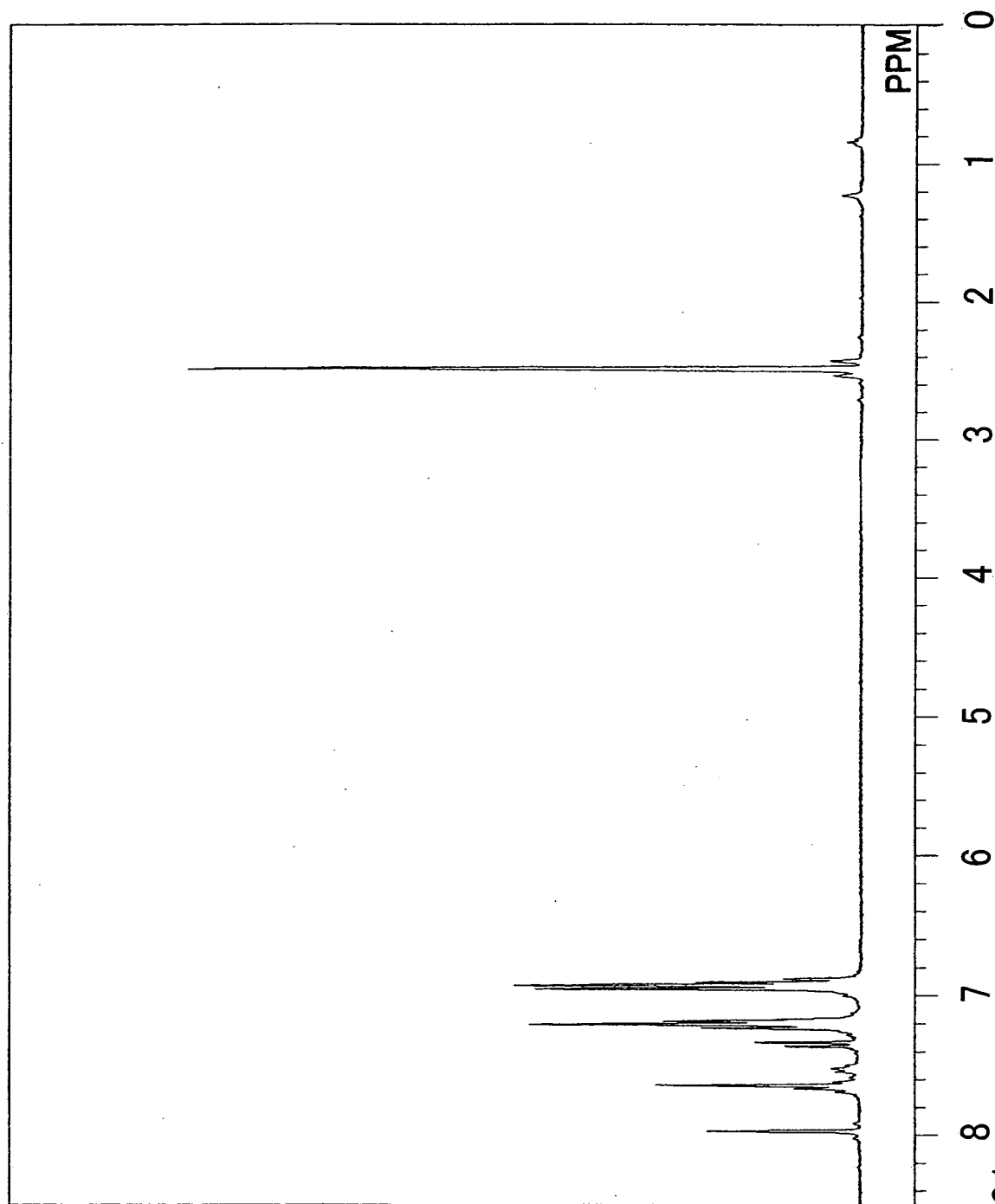


FIG. 21

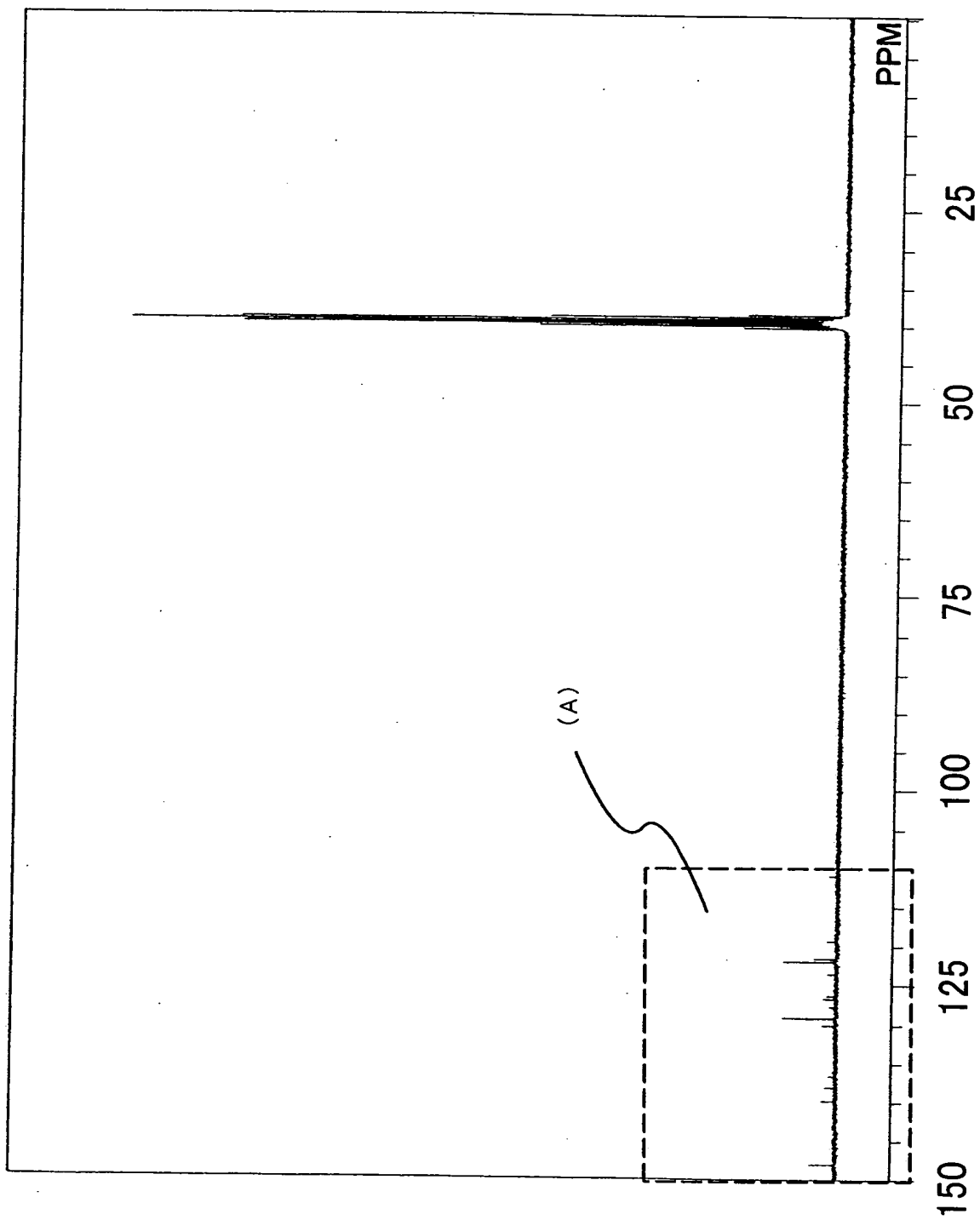


FIG. 22

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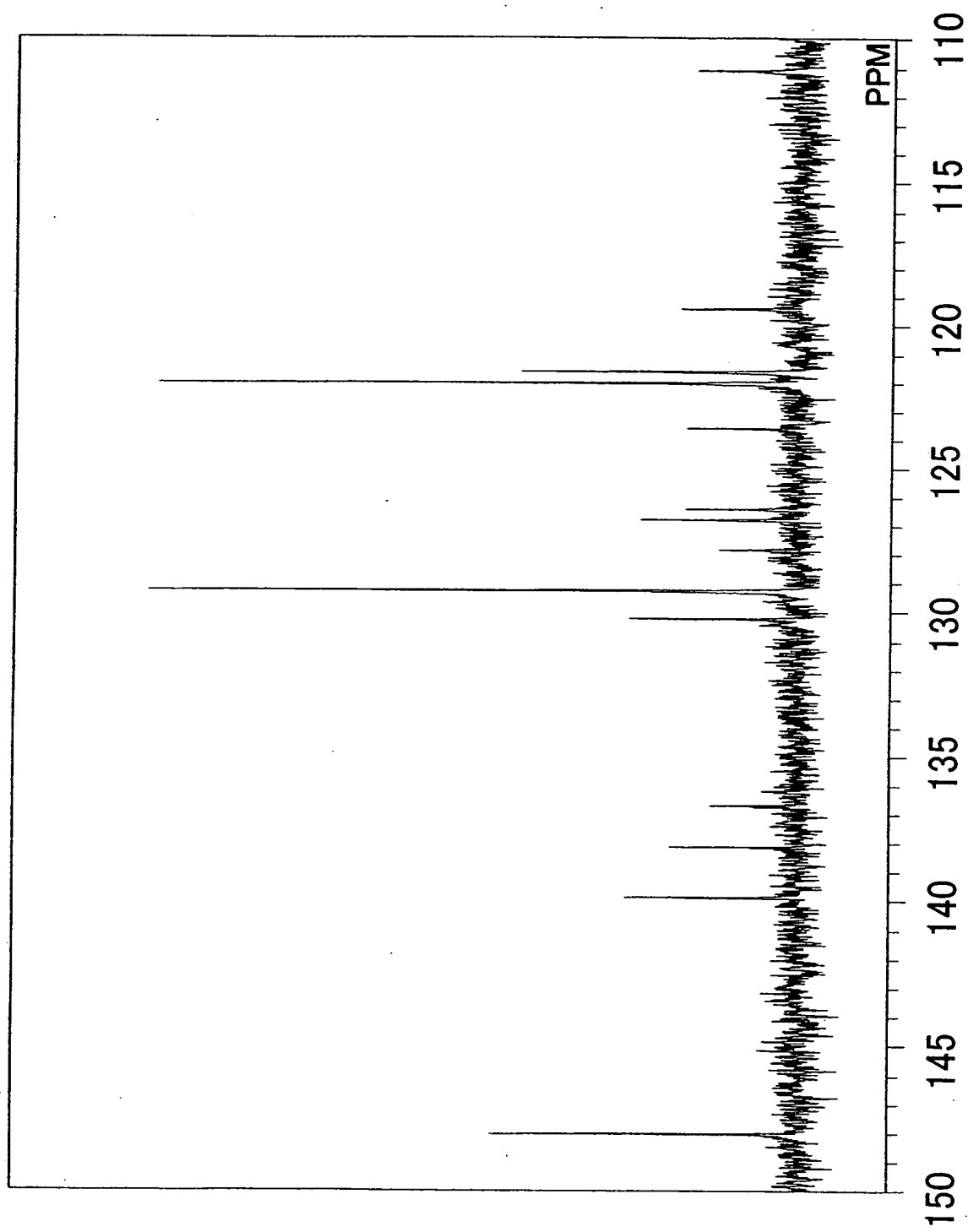


FIG. 23

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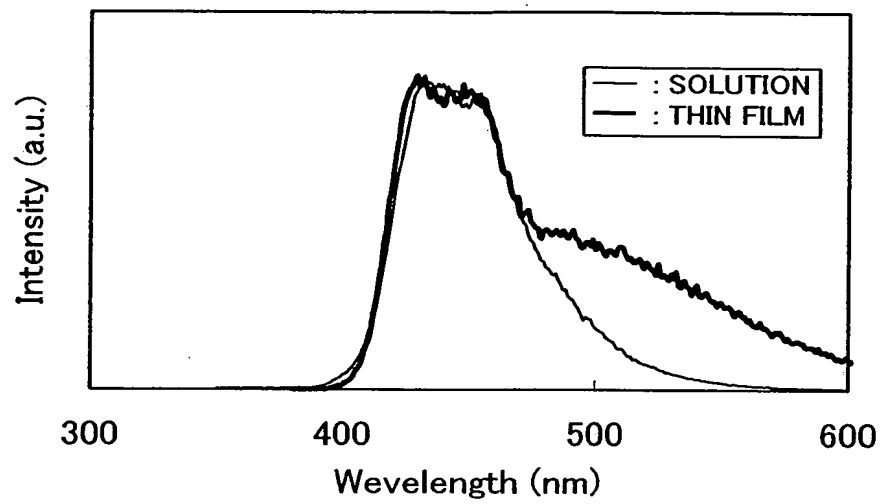


FIG. 24

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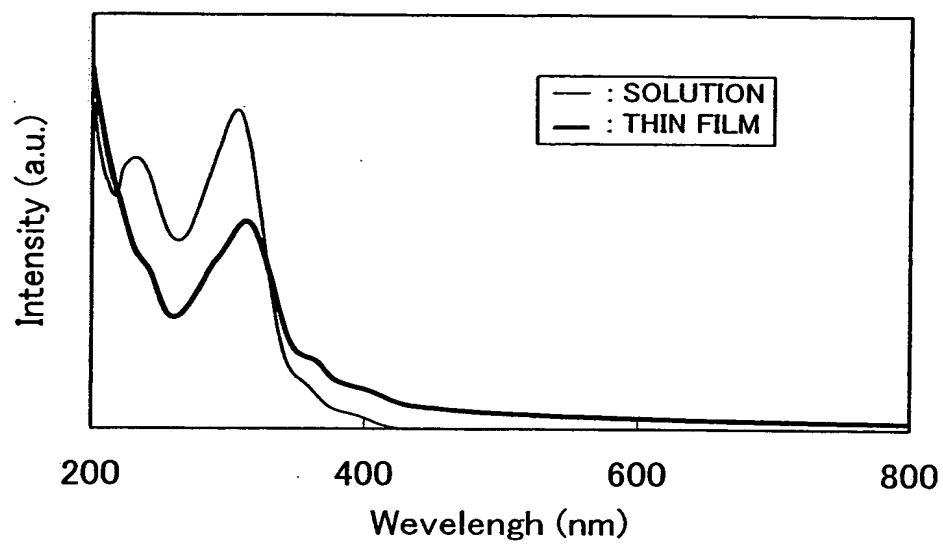


FIG. 25

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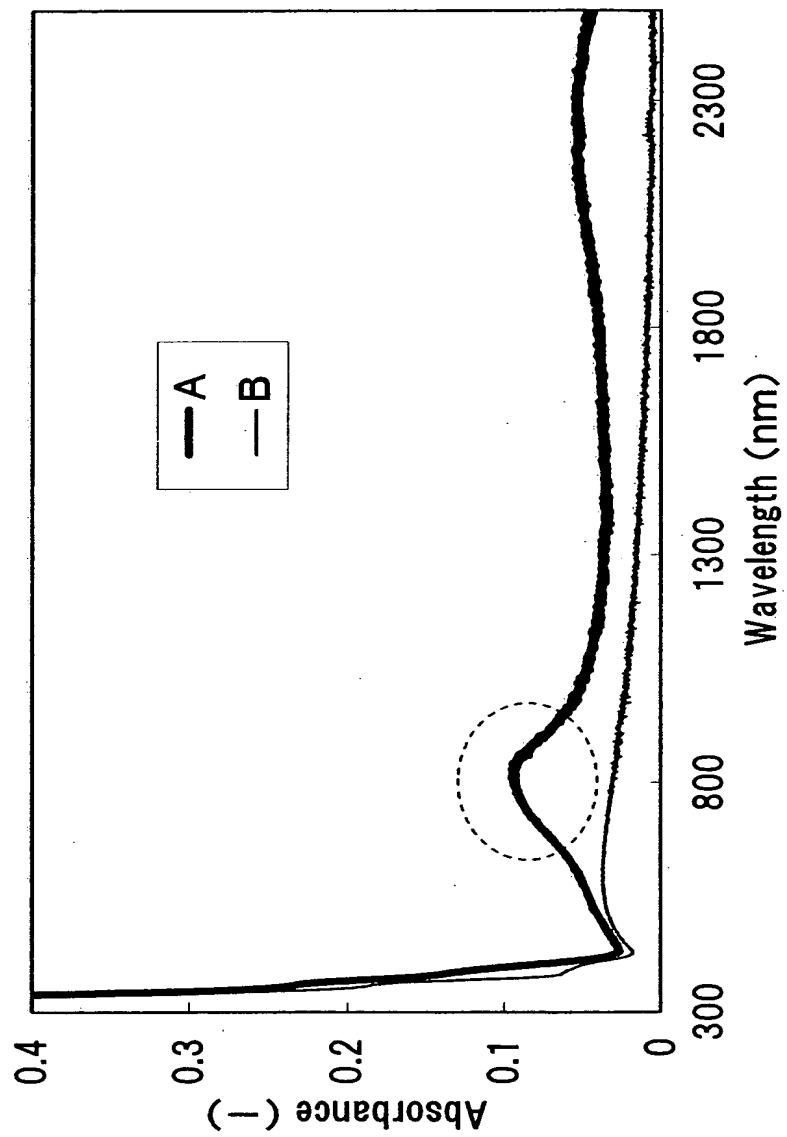


FIG. 26

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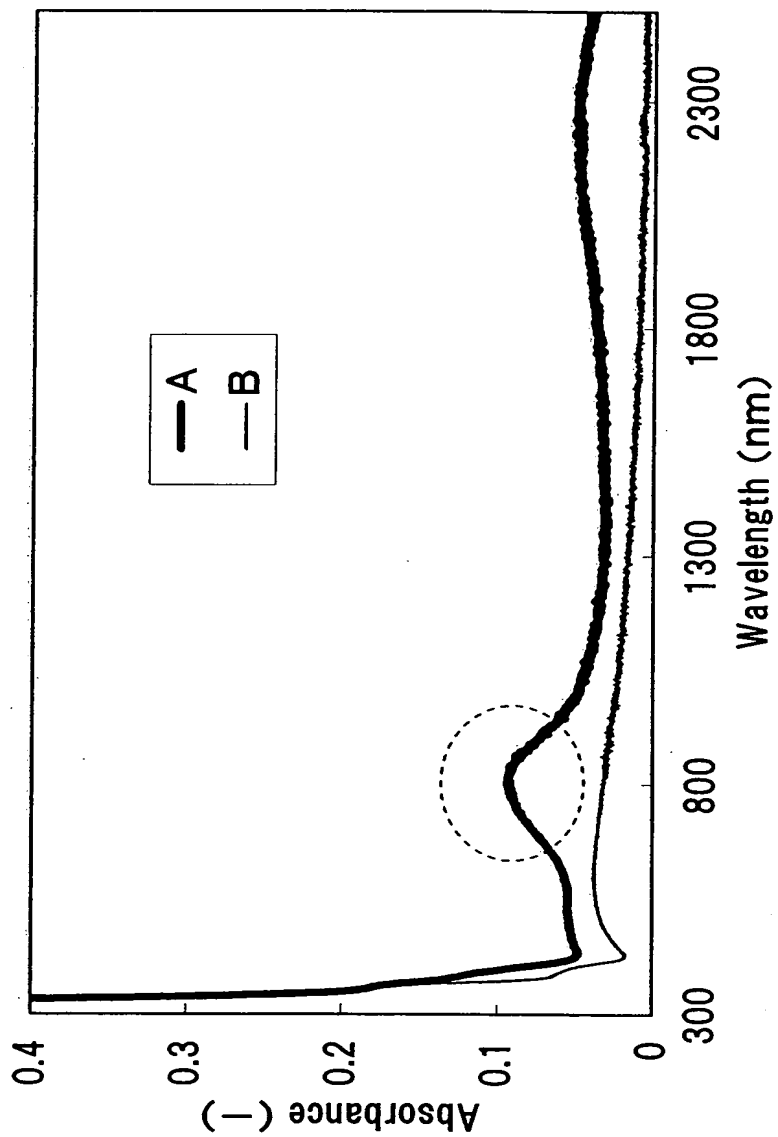


FIG. 27

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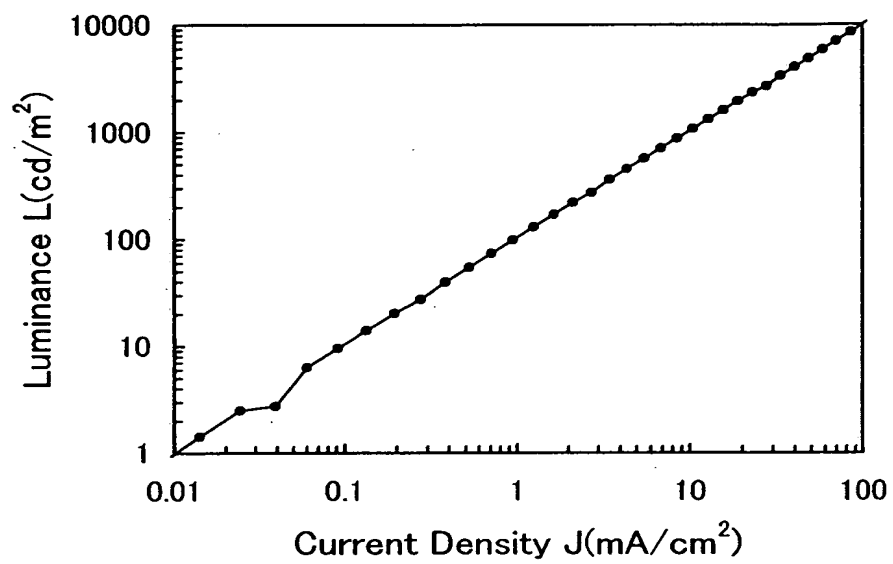


FIG. 28

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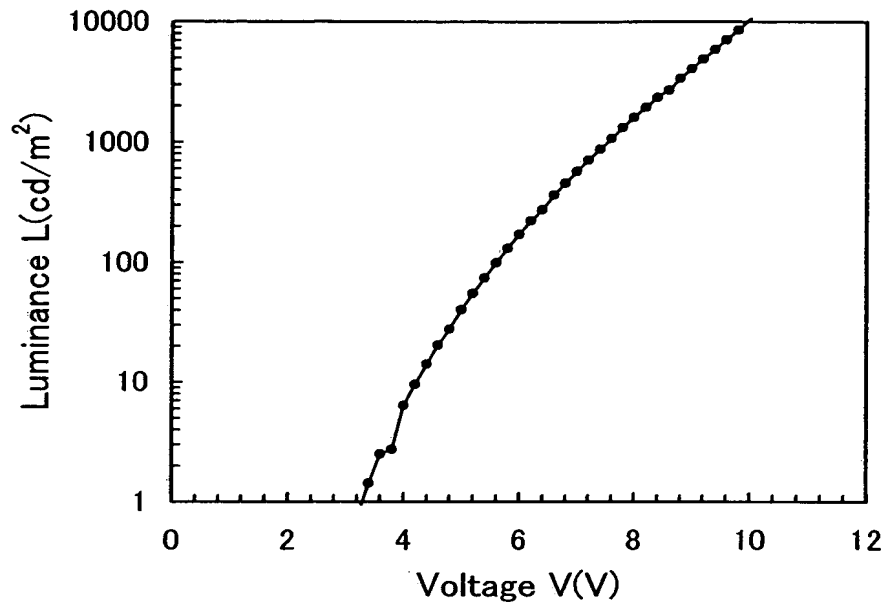


FIG. 29

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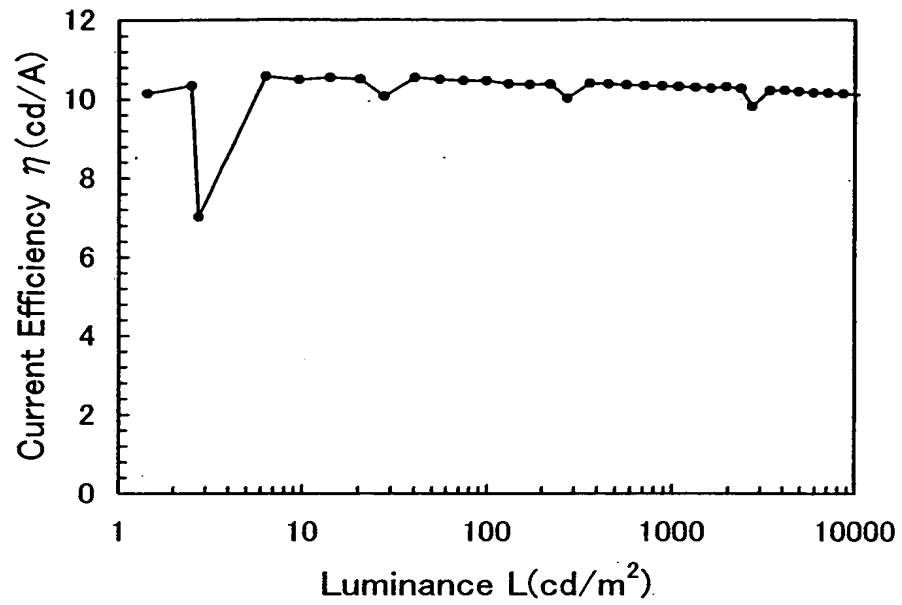


FIG. 30

EXPLANATION OF REFERENCE

101: First electrode, **102:** Second electrode, **111:** Hole generating layer, **112:** Hole transporting layer, **113:** Light emitting layer, **114:** Electron transporting layer, **115:** Electron generating layer, **116:** Electron injecting layer, **117:** Hole blocking layer, **201:** First electrode, **202:** Second electrode, **211:** Third layer, **212:** Second layer, **213:** First layer, **221:** Electron transporting layer, **222:** Light emitting layer, **223:** Hole transporting layer, **224:** Hole generating layer, **225:** Hole injecting layer, **226:** Hole blocking layer, **6500:** Substrate, **6503:** FPC, **6504:** Printed wiring board, **6511:** Pixel portion, **6512:** Source signal line driving circuit, **6513:** Writing gate signal line driver circuit, **6514:** Erasing gate signal line driver circuit, **901:** Transistor, **902:** Transistor, **903:** Light emitting element, **911:** Gate signal line, **912:** Source signal line, **913:** Writing gate signal line driver circuit, **914:** Erasing gate signal line driver circuit, **915:** Source signal line driver circuit, **916:** Power source, **917:** Current supply line, **918:** Switch, **919:** Switch, **920:** Switch, **1001:** Transistor, **1002:** Transistor, **1003:** Gate signal line, **1004:** Source signal line, **1005:** Current supply line, **1006:** Electrode, **501:** Sub-frame, **502:** Sub-frame, **503:** Sub-frame, **504:** Sub-frame, **501a:** Writing period, **501b:** Holding period, **502a:** Writing period, **502b:** Holding period, **503a:** Writing period, **503b:** Holding period, **504a:** Writing period, **504b:** Holding period, **504c:** Erasing period, **504d:** Non-light emitting period, **10:** Substrate, **11:** Transistor, **12:** Light emitting element, **13:** First electrode, **14:** Second electrode, **15:** Layer, **16:** Interlayer insulating film, **17:** Wiring, **18:** Partition wall layer, **19:** Interlayer insulating film, **951:** Substrate, **952:** Electrode, **956:** Electrode, **955:** Layer, **954:** Partition wall layer, **953:** Insulating layer, **5521:** Main body, **5522:** Chassis, **5523:** Display portion, **5524:** Keyboard, **5511:** Chassis, **5512:** Liquid crystal device, **5513:** Light emitting device, **5514:** Chassis, **5515:** External input terminal, **5516:** Light emitting element array, **5551:** Display portion, **5552:** Main body, **5553:** Antenna, **5554:** Voice output portion, **5555:** Voice input portion, **5556:** Operation switch, **5531:** Display portion, **5532:** Chassis, and **5533:** Speaker.